

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

|                 |            |       |          |          |                    |
|-----------------|------------|-------|----------|----------|--------------------|
| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART.UNIT | EXAMINER           |
| 09/841582       | F          | 257   | 688      | 2811     | <i>G. Hall Lee</i> |

## APPlicants

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Cmz

TIME

Chip-like electronic components, a method of manufacturing the same, a pseudo wafer therefor and a method of manufacturing thereof

PTO-2040  
12/98

## **ISSUING CLASSIFICATION**

| TERMINAL<br>DISCLAIMER  | DRAWINGS     |             |            | CLAIMS ALLOWED             |                      |
|---|--------------|-------------|------------|----------------------------|----------------------|
|   | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims               | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent<br>subsequent to _____ (date)<br>has been disclaimed.                      |              |             |            | NOTICE OF ALLOWANCE MAILED |                      |
| <input type="checkbox"/> The term of this patent shall<br>not extend beyond the expiration date<br>of U.S Patent. No. _____ |              |             |            | ISSUE FEE                  |                      |
| <input type="checkbox"/> The terminal _____ months of<br>this patent have been disclaimed.                                  |              |             |            | Amount Due                 | Date Paid            |
|   |              |             |            | ISSUE BATCH NUMBER         |                      |

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